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Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re A	Application of:)		
	••)	Examiner:	Vo, T.
	Wang, et al.)		
)	Art Unit:	2112
Serial No.: 10/706,675)		
)		
Filed: November 12, 2003)		
)		
For:	Efficient Bus Utilization in a Multiprocessor)		
	System by Dynamically Mapping Memory)		
	Addresses)		
Mail S	Stop Issue Fee			
P.O. B	3ox 1450			
Alexa	ndria, VA 22313-1450			

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Applicants are assuming that the Examiner's statement of reasons for allowance is to be taken in light of the structure and interaction recited in the claims. Applicants note that the Examiner's various comments should not be used to read non-existent limitations into the claims.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date! Mach 2,2005

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